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10/823,713

04/14/2004

Jae-Bon Koo

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05/05/2006

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SUITE 7500
VIENNA, VA 22182

EXAMINER

SEFER, AHMED N

ART UNIT

PAPER NUMBER

2826

DATE MAILED: 05/05/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/823,713

Applicant(s)

KOO ET AL.

Examiner

A. Sefer

Art Unit

2826

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER; FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 09 February 2006.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-5 and 7-33 is/are pending in the application.
- 4a) Of the above claim(s) 5,7-10 and 13-33 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-4,11 and 12 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

Response to Amendment

1. The amendment filed February 9, 2006 has been entered and claim 6 has been cancelled.

Response to Arguments

2. Applicant's arguments filed 2/9/2006 have been fully considered but they are not persuasive since the 35 U.S.C. 102(e) rejection of claims 1-4, 6, 11 and 12 based on Park et al. ("Park") US PG-Pub 2004/0066147 can be overcome by perfecting the claim to priority under 35 U.S.C. 119(a)-(d) within the time period set in 37 CFR 1.55(a)(1) or filing a grantable petition under 37 CFR 1.55(c). See MPEP § 201.13. **The filing date of the priority document is not perfected** unless applicant has filed a certified priority document in the application (and an **English language translation, if the document is not in English**) (see 37 CFR 1.55(a)(3)) and **the examiner has established that the priority document satisfies the enablement and description requirements of 35 U.S.C. 112, first paragraph.**

Claim Rejections - 35 USC § 102

3. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

4. Claims 1 and 2 are rejected under 35 U.S.C. 102(e) as being anticipated by Park.

The applied reference has a common assignee with the instant application. Based upon the earlier effective U.S. filing date of the reference, it constitutes prior art under 35 U.S.C. 102(e). This rejection under 35 U.S.C. 102(e) might be overcome either by a showing under 37 CFR 1.132 that any invention disclosed but not claimed in the reference was derived from the inventor of this application and is thus not the invention "by another," or by an appropriate showing under 37 CFR 1.131.

Park discloses (figs. 1-8 and par. 0063) a flat panel display, comprising: a pixel array portion 150 having a plurality of pixels arranged thereon; and a driving circuit portion 155 for driving the plurality of pixels of the pixel array portion, wherein a thin film transistor in the pixel array portion comprising a plurality of channel regions 323/327 corresponding to multiple gates 341/345 and having a different resistance value than a thin film transistor in the driving circuit portion.

Regarding claim 2, Park discloses the pixel array portion and the driving circuit portion each having a plurality of thin film transistors and where at least one thin film transistor of the plurality of thin film transistors in the pixel array portion has a resistance value higher than any of the plurality of thin film transistors in the driving circuit portion.

5. Claims 3 and 4 are rejected under 35 U.S.C. 102(e) as being anticipated by Park.

Park discloses (figs. 1-8 and par. 0063) a flat panel display, comprising: a pixel array portion 150 having a plurality of pixels arranged thereon; and a driving circuit portion 155 for driving the plurality pixels of the pixel array portion, wherein a thin film transistor in the pixel array portion comprising a plurality of channel regions 323/327 corresponding to multiple gates

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341/345 and having a different resistance value in its gate region than a thin film transistor in the driving circuit portion.

Regarding claim 4, Park discloses (pars. 45 and 48) a thin film transistor in the pixel array portion including an offset region 330 in its gate region positioned between a first channel region and a second channel region, the offset region having a higher resistance than a thin film transistor in the driving circuit portion.

6. Claims 11 and 12 are rejected under 35 U.S.C. 102(e) as being anticipated by Park.

Park discloses (figs. 1-8 and par. 0063) a flat panel display, comprising: a pixel array portion 150 having a plurality of pixels arranged thereon; and a gate driving circuit portion 155 and a data driving circuit portion (see data line 120) for driving the plurality of pixels of the pixel array portion, wherein at least one thin film transistor of a plurality of thin film transistors in the pixel array portion comprising a plurality of channel regions 323/327 corresponding to multiple gates 341/345 has a different resistance value from at least one thin film transistor of a plurality of thin film transistors in the gate driving circuit portion and the data driving circuit portion.

Regarding claim 12, Park discloses the at least one thin film transistor of the plurality of thin film transistors in the pixel array portion including an offset region 330 in its gate region positioned between a first channel region and a second channel region.

Conclusion

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

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A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

NATHAN J. FLYNN
SUPERVISORY PATENT
TECHNOLOGY

Any inquiry concerning this communication or earlier communications from the examiner should be directed to A. Sefer whose telephone number is (571) 272-1921.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan Flynn can be reached on (571) 272-1915.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

ANS

April 24, 2006